

FIG. 1

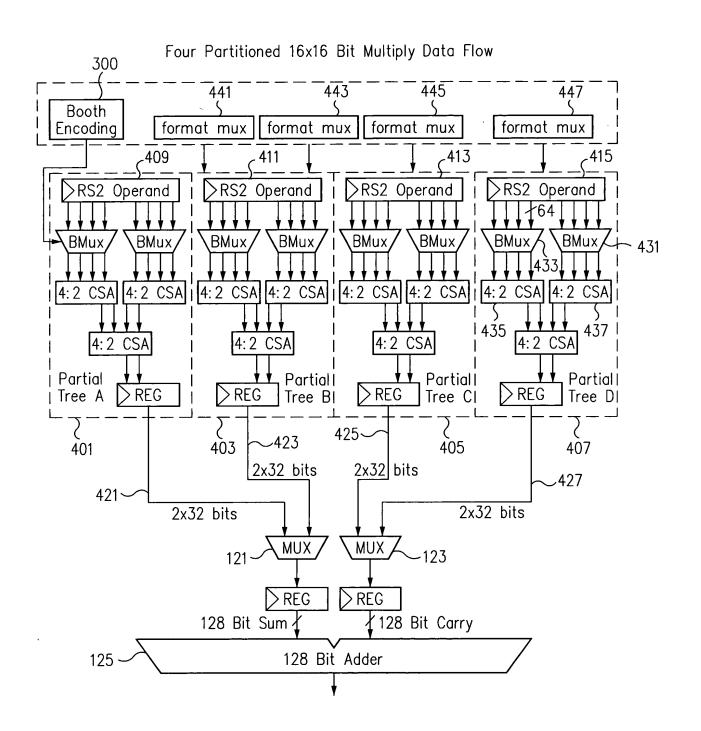


FIG. 4

Two Partitioned 32x32 Bit Multiply Data Flow

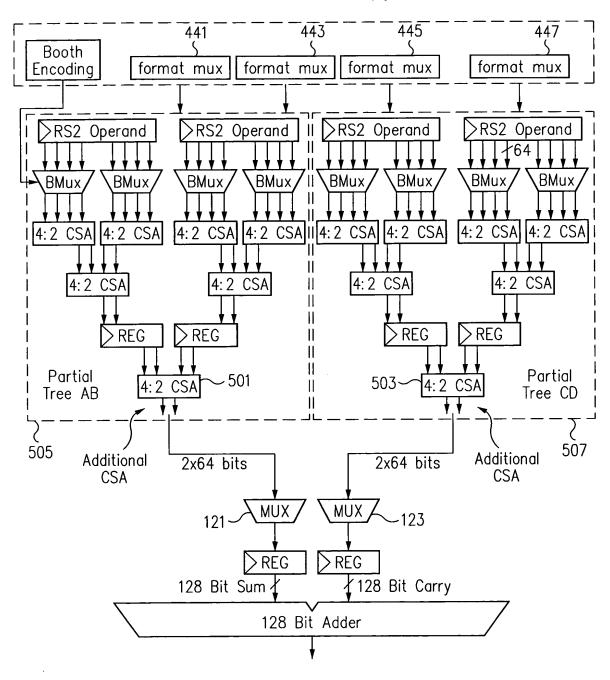


FIG. 5

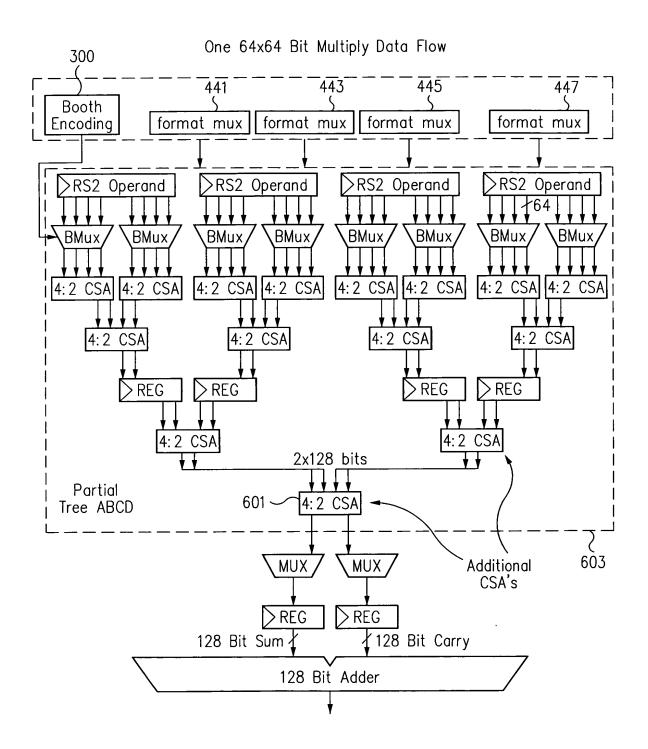


FIG. 6

Multiplier Structure Supporting Different Precision Multiplication Operations Jagodik et al 10/782,162

22.33.44.5.678901234567890	1P PP07(32 0) 0H000000 0H PP07(32 0) 0H000000 0H PP08(32 0) 0H	0000001P	PP12(320)
Two Single Precision (32x32) Multiplies		000000 0000 P. 00 1P. 00000 P. 00 1P. 1P.	00001P
Тwo	FIG. 8	00 00 01 18 000000 1P	00001P 001P 1P 012345678901

Multiplier Structure Supporting Different Precision Multiplication Operations Jagodik et al 10/782,162

One Double Precision (64x64) Multiply 012345678901234567	0000001P	0000001P PP20(64.0) Undocoded 00001P PP22(64.0) 0H000 001P PP22(64.0) 0H000 1P PP24(64.0) 0H00 00001P PP25(64.0) 0H00 1P PP26(64.0) 0H00 1P PP27(64.0) 0H00 1P PP28(64.0) 0H00 1P PP28(64.0) 0H00 1P PP28(64.0) 0H00 1P PP38(64.0) 0H00 1P 0H00 0H00 1
One Double		0000 00016 0016 0017 00017 00018 0018 0018 0018 0018 00
	FIG. 9	00 00 00 1F 0001P 1P



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